



ecc nand flash controller

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US Pat. 6683817 - Filed Feb 21, 2002 - QUALCOMM, Incorporated

Initially, the DMS controller within the EMI unit is loaded with the address ...

The ECC value stored in the spare area of the NAND Flash for the selected ...

**[APPLICATION] Memory controller interface**

US Pat. App 11/051,491 - Filed Feb 4, 2005

Transfers to NAND Flash 116 are preferably encrypted by AES and have ECC generated.

A NAND Flash controller 214, shown in FIG. ...

**Robust and high-speed memory access with adaptive interface timing**

US Pat. 7061804 - Filed Nov 18, 2004 - QUALCOMM Incorporated

3 shows a block diagram of an embodiment of NAND Flash controller 140 with ECC capability. NAND Flash controller 140 includes a control unit 310, an ECC ...

**[APPLICATION] Robust and high-speed memory access with adaptive interface timing**

US Pat. App 10/993,034 - Filed Nov 18, 2004

3 shows a block diagram of an embodiment of NAND Flash controller 140 with ECC capability. NAND Flash controller 140 includes a control unit 310, an ECC ...

**Computer system with NAND flash memory for booting and storage**

US Pat. 7234049 - Filed Jul 28, 2003 - Samsung Electronics Co., Ltd.

Referring to out from the NAND flash memory 10 and transmitted to the FIG.

2, the computer system includes a system controller 11 system controller 11, ...

**[APPLICATION] NAND flash memory controller exporting and NAND interface**

US Pat. App 11/326,336 - Filed Jan 6, 2006

For example, a NAND controller having a 1-bit ECC can work with SBC NAND ...

NAND controller and successive generations of NAND flash memory devices. ...

**[APPLICATION] Method for streamlining error connection code computation while reading or ...**

US Pat. App 10/764,670 - Filed Jan 26, 2004 - Texas Instruments Incorporated

Another ECC may be generated when reading the particular data from the NAND ...

2 illustrates a second block diagram of a NAND Flash memory controller, ...

Error correction cache for flash memory

US Pat. 7296213 - Filed Dec 11, 2002 - Nvidia Corporation

In another embodiment, DMA controller 108 is part of a NAND-flash controller that further includes the ECC processor. DMA controller 108 moves data from a ...

[APPLICATION] ECC control apparatus


US Pat. App 10/787,183 - Filed Feb 27, 2004

[0028] The ECC controller 1 and the NAND flash memory module 2 are connected by a data bus FDN[7:0] (hereinafter called "bus FDN"), a signal line 8, ...

[APPLICATION] Error correction for flash memory

US Pat. App 10/316,462 - Filed Dec 11, 2002

controller 108 and memory interface 106. In another embodiment, DMA controller 108 is part of a NAND-flash controller that further includes the ECC ...

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